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SPECIFICATION FOR APPROVAL

- () Preliminary Specification
- (
) Final Specification
 - Title

26.0" WUXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC260EUN
SUFFIX	SCA1 (RoHS Verified)

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE					
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Please return 1 conv for your	confirmation with					
Please return 1 copy for your confirmation with your signature and comments.						

APPROVED BY	SIGNATURE DATE				
H.S.SONG / Team Leader					
REVIEWED BY					
S.J.PARK / Project Leader					
PREPARED BY					
J.T.KIM / Engineer					
TV Product Development Dept. LG Display Co., Ltd					

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LC260EUN

Product Specification

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
1.0	May,10, 2010	-	Final Specification.

1. General Description

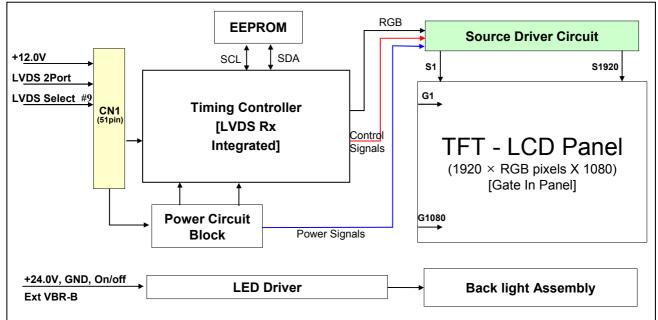
The LC260EUN is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element.

It is a transmissive display type which is operating in the normally black mode. It has a 26.02 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M (true) colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	26.02 inches(660.87mm) diagonal				
Outline Dimension	613 mm(H) x 361.0 mm(V) x 14.9 mm(D)[16.2 mm(User CNT)] (Typ.)				
Pixel Pitch	0.3 mm x 0.3 mm				
Pixel Format	1920 horiz. by 1080 vert. pixels RGB stripe arrangement				
Color Depth	8bit, 16,7 M colors				
Luminance, White	400 cd/m ² (Center 1 point) (Typ.)				
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Min.), U/D 178(Min.))				
Power Consumption	Total 38.38 Watt (Logic=5.88 W , LED Driver = 32.5W @ [ExtVbr_B=100%])				
Weight	2,750g(Typ.)				
Display Operating Mode	Transmissive mode, normally black				
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)				

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2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

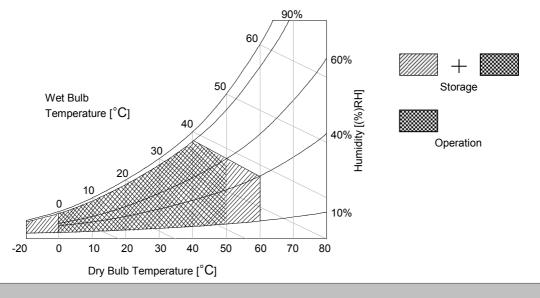
Para	Parameter		Va	lue	Unit	Note
Falai			Min	Max	Unit	Note
Dowor Input Voltago	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	VDC	
Driver Control Voltage	ON/OFF	Voff / Von	-0.3	+5.5	VDC	1
	Brightness	EXTVBR-B	0.0	+5.5	VDC	
T-Con Option Selection	Voltage	VLOGIC	-0.3	+4.0	VDC	
Operating Temperature		Тор	0	+50	°C	0.0
Storage Temperature	Storage Temperature		-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	0.0
Storage Humidity		Hs⊤	10	90	%RH	2,3

Note1. Ambient temperature condition (Ta = 25 \pm 2 $^\circ\text{C}$)

2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.



3. Electrical Specifications

3-1. Electrical Characteristics

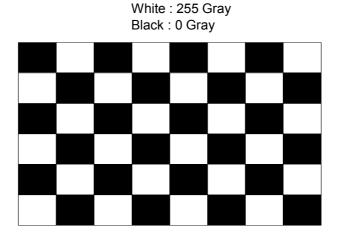
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note	
	Cymbol	Min	Тур	Max	Onic	Note
Circuit :						
Power Input Voltage	V _{LCD}	10.8	12.0	13.2	V _{DC}	
Dower Input Current	I _{LCD}	-	490	637	mA	1
Power Input Current		-	696	905	mA	2
Power Consumption	P _{LCD}	-	5.88	7.64	Watt	1
Rush current	I _{RUSH}	-	-	4.0	A	3

Notes : 1. The specified current and power consumption are under the V_{LCD}=12.0V, $25 \pm 2^{\circ}$ C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).



Mosaic Pattern(8 x 6)

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

De	rameter		Symbol		Values		Unit	Notes	
Fa	lameter		Symbol	Min	Тур	Max	Unit	NOLES	
LED Driver :									
Power Supply Inpu	t Voltage		VBL	22.8	24.0	25.2	Vdc	1	
Power Supply Input	Current		IBL_A	-	1.35	1.45	А	1	
Power Supply Input Current (In-Rush)		Irush	-	-	3.0	А	VвL = 22.8V Ext VвR-в = 100%		
Power Consumptio	Power Consumption		PBL	-	32.5	34.8	W	1	
	On/Off	On	V on	2.5	-	5.0	Vdc		
		Off	V off	-0.3	0.0	0.7	Vdc		
Input Voltage for	Brightness	Adjust	ExtVBR-B	10	-	100	%	On Duty	
Control System	PWM Frequ NTSC & PA	ency for	PAL		100		Hz	3	
Signals	NTSC & PA	L	NTSC		120		Hz	3	
	Pulse Duty	Level	High Level	2.5	-	5.0	Vdc	HIGH : on duty	
(PWM)		Low Level	0.0	-	0.7	Vdc	LOW : off duty		
LED :									
Life Time				30,000	50,000		Hrs	2	

Notes :

 Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B : 100%), it is total power consumption.

2. The life time(MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at 25±2°C.

3. LGD recommend that the PWM freq. is synchronized with Two time harmonic of Vsync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.

4. The duration of rush current is about 10ms.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

-LCD Connector(CN1): IS050-C51B-C39-A(manufactured by UJU) or compatible

- Mating Connector : FI-RE51HL(JAE) or compatible

No	Symbol	Description		No	Symbol	Description
1	NC	No Connection		27	NC	No Connection
2	NC	No Connection		28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection		29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection	ŀ	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection		31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection	⊢	32	R2DF	SECOND LVDS Receiver Signal (C-)
7	LVDS Select			32	-	
		'H' =JEIDA , 'L' or NC = VESA	L		R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection		34	GND	Ground
9	NC	No Connection	Ļ	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection	Ļ	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground		37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)		38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	Í	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	ĺ	40	NC	No Connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	ĺ	41	NC	No Connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	ĺ	42	Reserved	No connection or GND
17	R1CP	FIRST LVDS Receiver Signal (C+)	ĺ	43	Reserved	No connection or GND
18	GND	Ground	ĺ	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)		45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	ĺ	46	GND	Ground
21	GND	Ground	ĺ	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)		48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)		49	VLCD	Power Supply +12.0V
24	NC	No Connection	Î	50	VLCD	Power Supply +12.0V
25	NC	No Connection	1	51	VLCD	Power Supply +12.0V
26	Reserved	No connection or GND		-	-	-

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Notes : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

2. All VLCD (power input) pins should be connected together.

- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module. If not used, these pins are no connection.
- Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Inverter

- LED Connector : SM14B-SRSS-TB(Manufactured by JST)
- Mating Connector : SHR-14V-S-B(With protrusions) or SHR-14V-S(Without protrusions) ; (Manufacture by JST)

Table 5. INVERTER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	NC	No Connection	
12	VON/OFF	Backlight ON/OFF control	
13	EXTVBR-B	External PWM	2
14	Status	LED Status	3

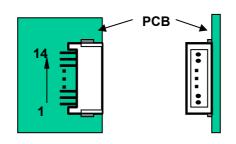
Notes :

- 1. GND should be connected to the LCD module's metal frame.
- 2. High : on duty / Low : off duty
- 3. Normal : Low (under 0.7V) / Abnormal : High (upper 3.0V)

If status isn't used, it is recommended to be open.

4. Each impedance of 12 and 13 is over 50 [K Ω] and over 50[K Ω].

• Rear view of LCM



3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

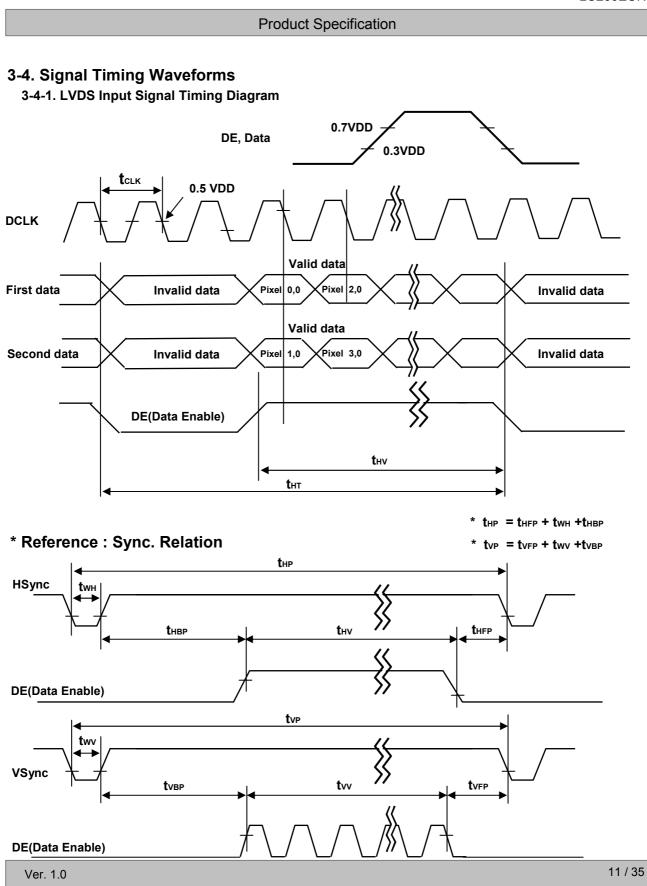
I	TEM	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	-	Lines	
Vertical	Blank	t∨в	11	45	69	Lines	
	Total	tvp	1091	1125	1149	Lines	
I	TEM	Symbol	Min	Тур	Мах	Unit	Note
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	f∨	57	60	63	Hz	

Table 7 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

I	ТЕМ	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tн∨	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	t∨∨	-	1080	-	Lines	
Vertical	Blank	tvв	228	270	300	Lines	
	Total	tvp	1308	1350	1380	Lines	
I	TEM	Symbol	Min	Тур	Мах	Unit	Note
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	fv	47	50	53	Hz	

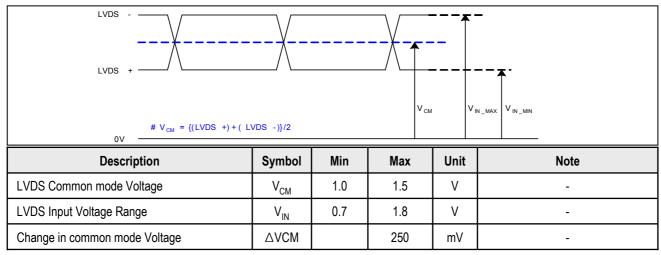
Table 7. TIMING TABLE for PAL (DE Only Mode)

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). <u>The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.</u>

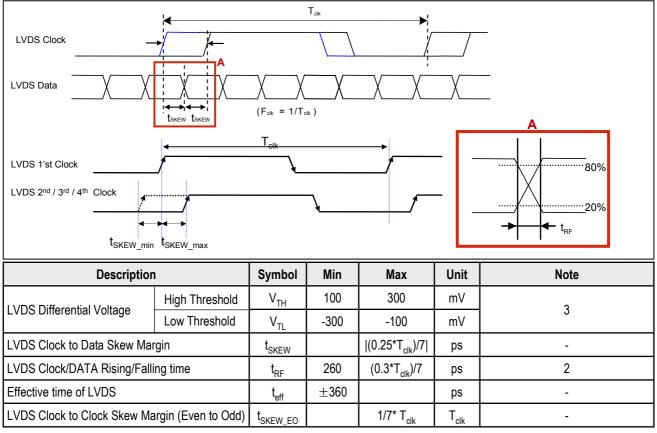


3-4-2. LVDS Input Signal Characteristics

1) DC Specification



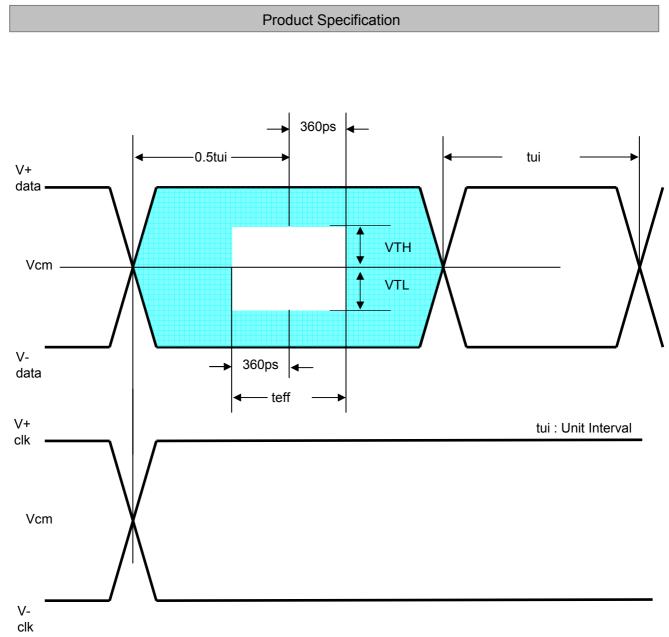
2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range.
- 3. LVDS Differential Voltage is defined within t_{eff}

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3-5. Color Data Reference

The brightness of each primary color (Red, Green, Blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

													Inpu	ıt Co	olor	Data	а		-							
Color				_		RE	D					_		GRE	EEN					_		BL	UE			
			MS								MS								MS							SB
			R7		R5	R4	R3	R2	R1	R0					G3	G2		G0		B6		B4		B2		B0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																										
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) I	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

3-6-1. LCD Driving circuit

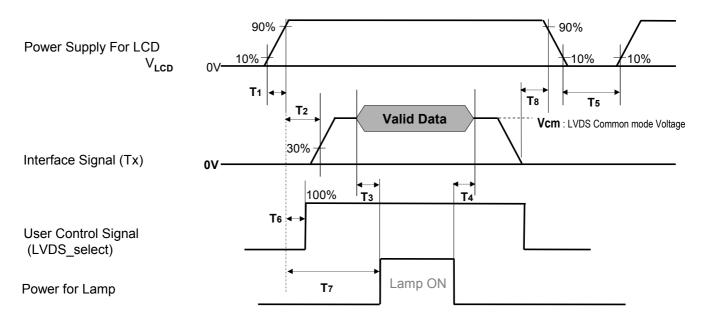


Table 8. POWER SEQUENCE

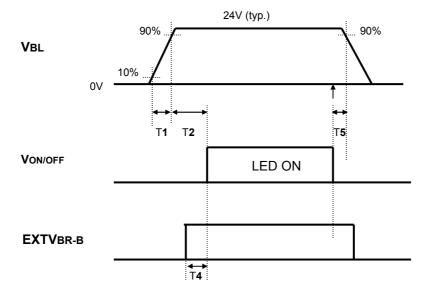
Devementer		Value							
Parameter	Min	Тур	Мах	Unit	Notes				
T1	0.5	-	20	ms					
T2	0	-	-	ms	4				
Т3	200	-	-	ms	3				
Τ4	200	-	-	ms	3				
T5	1.0	-	-	s	5				
T6	-	-	T2	ms	4				
T 7	0.5	-	-	S					
Т8	100	-	-	ms	6				

Note : 1. Please avoid floating state of interface signal at invalid period.

- 2. When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. If the on time of signals(Interface signal and user control signals) precedes the on time of Power(V_{LCD}), it will be happened abnormal display. When **T6** is NC status, **T6** doesn't need to be measured.
- 5. **T5** should be measured after the Module has been fully discharged between power off and on period.
- 6. It is recommendation specification that **T8** has to be 100ms as a minimum value.

3-6-2. Sequence for LED Driver

Power Supply For LED Driver



3-6-3. Dip condition for LED Driver

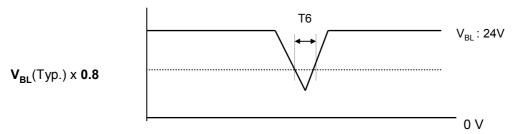


Table 9. Power Sequence for Inverter

Parameter		Values		Units	Remarks
Falameter	Min Typ		Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
T4	0		-	ms	
T5	10	-	-	ms	
Т6	-	-	10	ms	V_{BL} (Тур) х 0.8

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25±2°C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method in FIG. 1.

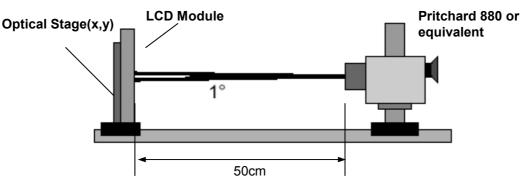


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25 \pm 2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, EXTVBR_B=100%

Parameter		0	h		Value		11	Nists
Pa	arameter	Sym	DOI	Min	Тур	Max	Unit	Note
Contrast Ratio		CR		700	1000	-		1
Surface Luminan	ice, white	Lw	н	320	400		cd/m ²	2
Luminance Varia	tion	δ_{WHITE}	5P	-	-	1.3		3
	Gray-to-Gray (BW)	G to C	З _{вw}	-	8	12	ms	4
Response Time	Variation	G to	G _σ		5	8	ms	5
		R	ĸ		0.633			
	RED	Ry	/		0.344			
		Gx Gy Bx By Wx Wy			0.307			
Color Coordinates	GREEN			Тур -0.03	0.603	Тур +0.03		
[CIE1931]	BLUE				0.153			
	BLUE				0.053	1		
					0.279			
	WHITE				0.292			
Color Temperatur	e				10,000		K	
Color Gamut					68		%	
Viewing Angle (C	CR>10)							
>	x axis, right(∳=0°)		-	89	-	-		
x axis, left (φ=180°) y axis, up (φ=90°)		θΙ		89	-	-	dograc	6
		θυ	ı	89	-	-	degree	6
У	ν axis, down (φ=270°)	θα	ł	89	-	-	-	
Gray Scale					-			7

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Product Specification

Notes : 1. Contrast Ratio (CR) is defined mathematically as :

- CR = Surface Luminance at all white pixels
 - Surface Luminance at all black pixels It is measured at center 1-point.
- Surface luminance is determined after the unit has been 'ON' and 1Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5})

Where $\rm L_{on1}$ to $\rm L_{on5}$ are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_R) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 3.
 - $\,$ % G to G_{\rm BW} Spec stands for average value of all measured points.

Photo Detector : RD-80S / Field : 2 °

5. G to G $_{\sigma}$ is Variation of Gray to Gray response time composing a picture

G to G (
$$\sigma$$
) = $\sqrt{\frac{\sum (Xi - u)^2}{N}}$ Xi = In
u = D

Xi = Individual Data u = Data average N : The number of Data

- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
LO	0.10
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Measuring point for surface luminance & measuring point for luminance variation.

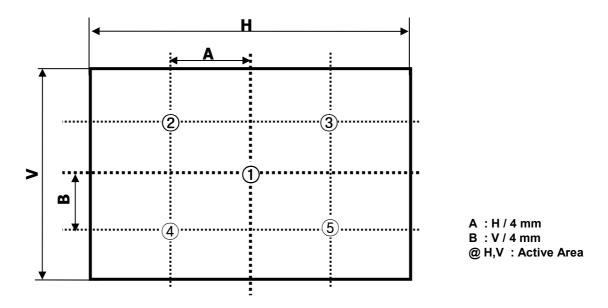


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

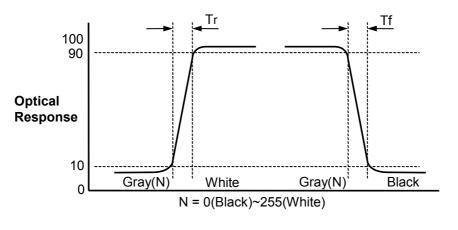


FIG. 3 Response Time

Dimension of viewing angle range

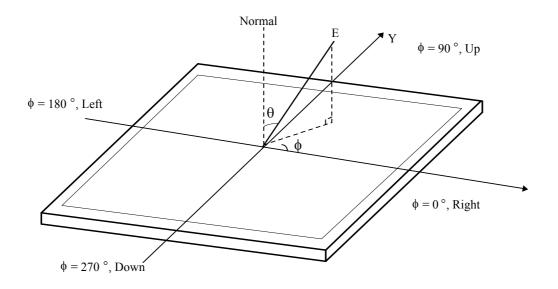


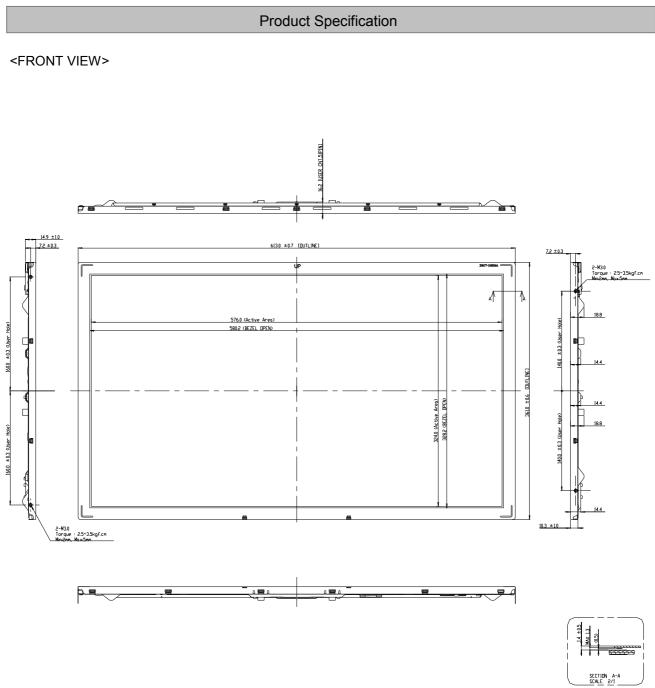
FIG. 4 Viewing Angle

5. Mechanical Characteristics

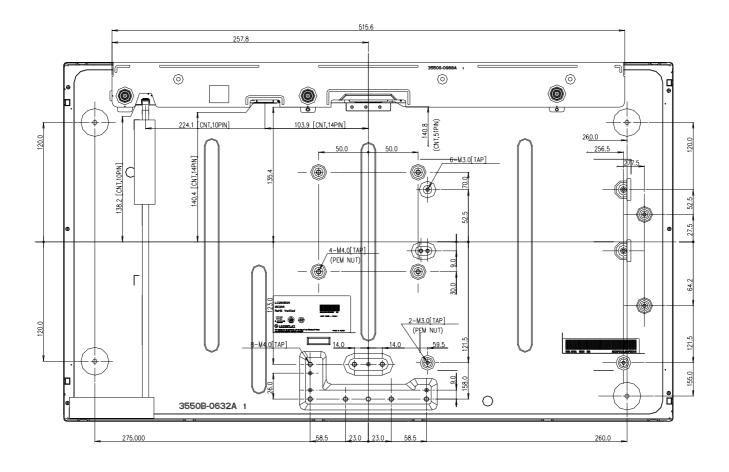
Table 12 provides general mechanical characteristics.

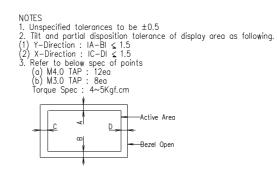
Item		Value				
	Horizontal	613.0mm				
Outline Dimension	Vertical	361.0 mm				
	Depth	14.9 mm [16.2mm User CNT]				
Devel Area	Horizontal	580.2mm				
Bezel Area	Vertical	328.2mm				
Antiva Diantau Avan	Horizontal	575.769mm				
Active Display Area	Vertical	323.712mm				
Weight	2,750g (Typ.), 2,900g (Max.)					

Note : 1.Please refer to a mechanical drawing in terms of tolerance at the next page.



<REAR VIEW>





6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min Each direction per 10 min
6	Shock test (non-operating)	Shock level : 100Grms Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC).
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
 (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

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Class 1 LED Product
IEC60825-1 : 2001
Embedded LED Power (Class 1M)
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7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

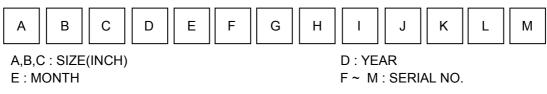
7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Packing

8-1. Information of LCM Label

a) Lot Mark



Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 8 pcs
- b) Box size : 690mm(W) X 350mm(D) X 442mm(H)

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1)You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 mV(Over and under shoot voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

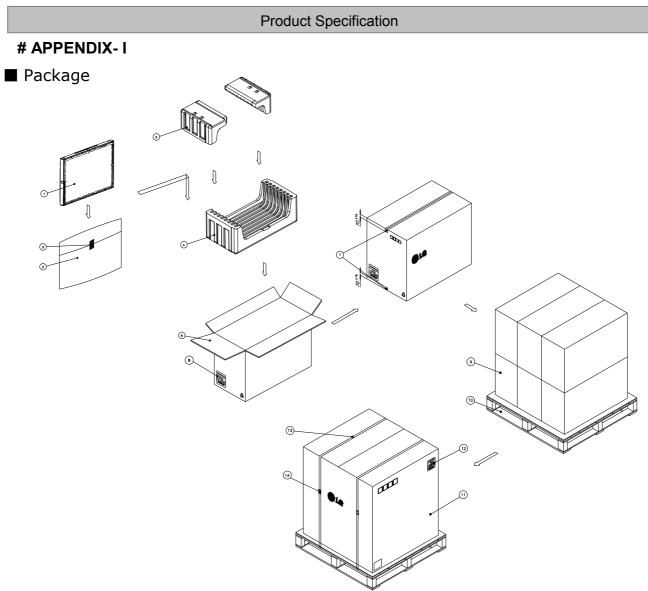
9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



% Packing Ass`y

℁ Pallet Ass`y

NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	PE
3	TAPE	MASKING 20MMX50M
4	Packing	EPS
5	Packing	EPS
6	вох	PAPER_SWR4
7	TAPE	OPP 70MMX300M
8	Label	ART 100X70

NO.	DESCRIPTION	MATERIAL
9	PACKING ASS'Y	
10	PALLET	Plywood
11	ANGLE, PACKING	PAPER (SWR4)
12	LABEL	PAPER
13	BAND	PP
14	CLIP, BAND	STEEL

Ver. 1.0

29 / 35



APPENDIX- II-2

Box Label

Pallet Label

	LC260EUN				
	SCA1				
8 pcs	001/01-01				
MADE IN	KOREA	RoHS	Verified		

	LC260EL	JN	
	SCA1		
48 pcs	001/01-01		
MADE IN	KOREA	RoHS	Verified

APPENDIX-III-1

Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter (Pin7="L or NC")

Host System 24 Bit	THC63LVD823 or Compatible	FI-R51S-HF	Timing Controller
R10/R20 R11/R21 R12/R22 R13/R23 R14/R24 R15/R25 R16/R26 R17/R27 G10/G20 G11/G21 G12/G22 G13/G23 G14/G24 G15/G25 G16/G26 G17/G27 B10/B20 B11/B21 B12/B22 B13/B23 B14/B24 B15/B25 B16/B26	54/02 TA1-TA1+ 57/83 TA1-TA1+ 58/84 TB1-/TB1+ 59/85 60/86 60/86 TC1-/TC1+ 51/79 TCLK1- 52/80 TCLK1+ 63/91 TD1-/TD1+ 64/92 65/93 66/94 TA2-/TA2+ 67/95 68/96 68/96 TB2-/TB2+ 61/89 62/90 73/99 TCLK2- 73/99 TCLK2-	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA1N RA1P RB1N RB1P RC1N RC1P RCLK1N RCLK1P RD1N RD1P RA2N RA2P RB2N RB2P RC2N RB2P RC2N RC2P RCLK2N RCLK2P RCLK2P RD2N RD2P VESA / JEIDA
B16/B26 B17/B27 Hsync Vsync Data Enable CLOCK	69/97 70/98 7 8 9	Ground LCC) Module

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX-III-1

Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter (Pin7="H")

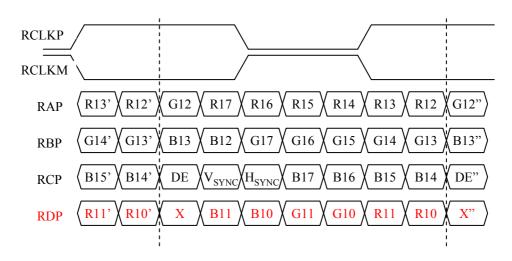
Host System 24 Bit	THC63LVD823 or Compatible	FI-R51S-HF	Timing Controller
R10/R20 R11/R21 R12/R22 R13/R23 R14/R24 R15/R25 R16/R26 R17/R27 G10/G20 G11/G21 G12/G22 G13/G23 G14/G24 G15/G25 G16/G26 G17/G27 B10/B20	51/79 52/80 53/81 TA1-TA1+ 53/82 TB1-/TB1+ 57/83 58/84 TC1-/TC1+ 59/85 TCLK1+ 60/86 TCLK1+ 61/89 TD1-/TD1+ 62/90 63/91 64/92 63/91 64/92 66/94 TB2-/TB2+ 67/95 68/96 TC2-/TC2+	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RA1N RA1P RB1N RB1P RC1N RC1P RCLK1N RCLK1P RD1N RD1P RD1P RA2N RA2P RB2N RB2P RB2N RB2P RB2N RB2P RC2N RC2P
B10/B20 B11/B21 B12/B22 B13/B23 B14/B24 B15/B25 B16/B26 B17/B27 Hsync Vsync Data Enable CLOCK	69/97 TCLK2- 70/98 TCLK2+ 73/99 TD2-/TD2+ 74/100 75/1 76/2 77/5 78/6 7 8 9 10	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	RCLK2N RCLK2P RD2N RD2P VESA / JEIDA

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

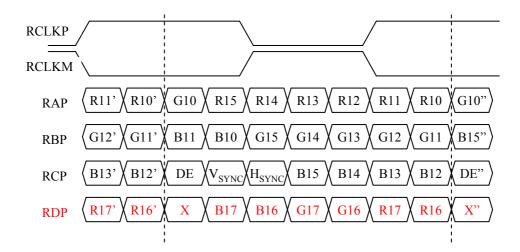
APPENDIX- IV

LVDS Data-Mapping info. (8bit)



LVDS Select : "H" Data-Mapping (JEIDA format)

LVDS Select : "L" Data-Mapping (VESA format)



APPENDIX- V

Option Pin Circuit Block Diagram

