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SPECIFICATION FOR APPROVAL

() Preliminary Specification

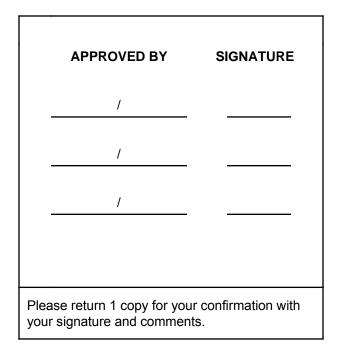
() Final Specification

Γ

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LP150X08
Suffix	TLAD

*When you obtain standard approval, please use the above model name without suffix



	SIGNATURE / DATE
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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID Ver.
1.0		-	First Draft (Preliminary)	
1.1	Nov. 8. 2007	4, 12	Luminance : 170nit -> 160nit	
		18	LCM rear view label change	
		22	Label change	
		25~27	EDID change (year: 2005 ->2007, model name: TLA2 -> TLAD,P/C)	
1.2	Dec. 5. 2007	9	Dclk min/max change(min : 65→62, max : 65→68)	
		-	Final CAS	

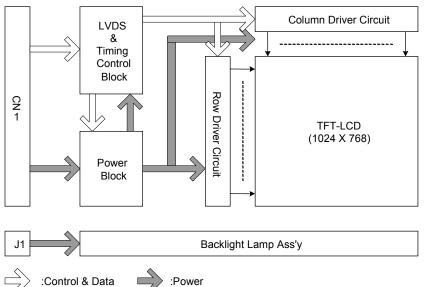


1. General Description

The LP150X08 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.0 inches diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150X08 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP150X08 is intended to support applications where thin thickness, low power are critical factors and graphic display are important. In combination with the vertical arrangement of the sub-pixels, the LP150X08 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.0 inches(38.1cm) diagonal
Outline Dimension	317.3(H) x 241.5(V) x 5.9(D) mm(Typ.)
Pixel Pitch	0.297 mm x 0.297 mm
Pixel Format	1024 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White (5P)	160 cd/m²(Typ.)
Power Consumption	4.76W (Typ.)
Weight	530 g (typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

2. Absolute Maximum Ratings

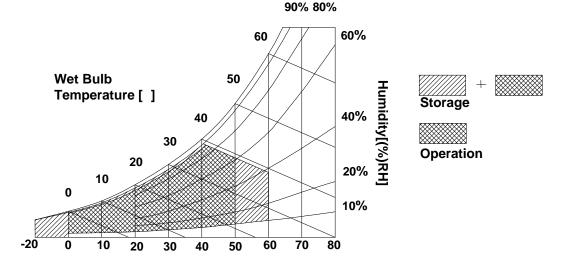
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Parameter	Symbol	Values		Units	Notes	
Faranielei	Symbol	Min	Max	Units	110105	
Power Input Voltage-ON	VCC	2.7	4.0	Vdc	at 25 \pm 5°C	
Power Input Voltage-OFF	GND	-0.3	0.3	Vdc	at 25 \pm 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Тѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	
Electrostatic Durability (ESD)	Vesd	± 8.0		kV	2	

Table 1. ABSOLUTE MAXIMUM RATINGS

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.

- 2. Condition 1) Non-operation, 150 -330Ω, 25 , 40~60%RH
 - 2) I/F Connector pins are subjected.
 - 3) The surface of Metal bezel and LCD are subjected.
 - 4) Discharge interval time 1sec, 10 times each place



Dry Bulb Temperature []

3. Electrical Specifications

3-1. Electrical Characteristics

The LP150X08 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Parameter		Symbol	Values			Unit	Notes
i aid	ameter	Symbol	Min	Тур	Max		NOICES
MODULE :							
Power Supply In	put Voltage	VCC	3.0	3.3	3.6	Vdc	
Power Supply In	put Current	I _{cc}	-	230	265	mA	1
Power Consump	otion	Pc	-	0.76	0.87	Watt	1
Differential Imp	bedance	Zm	90	100	110	ohm	2
LAMP :							
Operating Voltag	Operating Voltage		630	665	875	V _{RMS}	3
Operating Currer	Operating Current		2.0	6.0	7.0	mA _{RMS}	
Established Start	Established Starting Voltage						4
	at 25 °C		-	-	1165	V _{RMS}	
	at 0 °C		-	-	1400	V _{RMS}	
Operating Frequency		f _{BL}	50	65	80	kHz	5
Discharge Stabilization Time		Ts	-	-	3	Min	6
Power Consum	Power Consumption		-	3.9	4.3	Watt	7
Life Time			10,000	-	-	Hrs	8

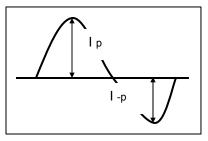
Table 2.	ELECTRICAL	CHARACTERISTICS
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Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.

- 1. The specified current and power consumption are under the VCC=3.3V, 25°C, f_v=60Hz condition whereas Mosaic pattern is displayed and f_v is the frame frequency.
- 2. This impedance value is needed to proper display and measured from LVDS T_X to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

- 5. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform. (Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 6. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
- 7. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is 6.0_{mA_{RMS}}.
- 8. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at 6.0mA_{RMS} on condition of continuous operating at $25 \pm 2^{\circ}\text{C}$
- 9. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 - It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2}\pm10\%.$
 - * Inverter output waveform had better be more similar to ideal sine wave.



LG.PHILIPS LCD

Do not attach a conducting tape to lamp connecting wire.

If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.



3-2. Interface Connections

The interface connections are compatible with ISP (Industry Standard Panels) 15.0" Mounting and Top Level Interface Requirements (Version2, June,2000) defined by SPWG (Standard Panels Working Group). This LCD employs two interface connections, a 30 pin connector is used for the module electronics and the other connector is used for the integral backlight system.

The electronics interface connector is a model GT101-30S-HR11 manufactured by LG Cable. The pin configuration for the connector is shown in the table below.

Pin	Symbol	Description	Notes
1	VSS	Ground	
2	VCC	Power Supply, 3.3V Typ.	[LVDS Transmitter]
3	VCC	Power Supply, 3.3V Typ.	TI, SN75LVDS84 or equivalent
4	VEDID	DDC 3.3V power	
5	NC	No Connection	
6	CIK ED D	DDC Clock	[LVDS Receiver]
7	DATAED D	DDC Data	UMC, FXLVRX085H90A
8	R _N 0-	- LVDS differential data input (R0-R5, G0)	
9	R _N 0+	+ LVDS differential data input (R0-R5, G0)	[Connector]
10	VSS	Ground	LCD : LG Cable GT101-30S-HR11
11	R _N 1-		*JAE FI-XB30Sx-HFxx or
12	R _N 1+	+ LVDS differential data input (G1-G5, B0-B1)	equivalent.
13	VSS	Ground	
14	R _N 2-		Matching : JAE FI-X30M or
15	R _N 2+		equivalent
16	VSS	Ground	
17	ClkIN -	- LVDS differential clock input	
18	ClkIN +	+ LVDS differential clock input	[Connector pin arrangement]
19	VSS	Ground	
20	NC	No Connection	
21	NC	No Connection	
22	VSS	Ground	
23	NC	No Connection	
24	NC	No Connection	LCD rear view
25	VSS	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	VSS	Ground	
29	NC	No Connection	
30	NC	No Connection	

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Note: All GND(ground) pins should be connected together and to GND which should also be connected to the LCD's metal frame. All VCC (power input) pins should be connected together.

The backlight interface connector is JST BHSR-02VS-1 or equivalent. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (J1)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes : 1. The high voltage side terminal is colored pink and the low voltage side terminal is Green.



3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for it's proper operation.

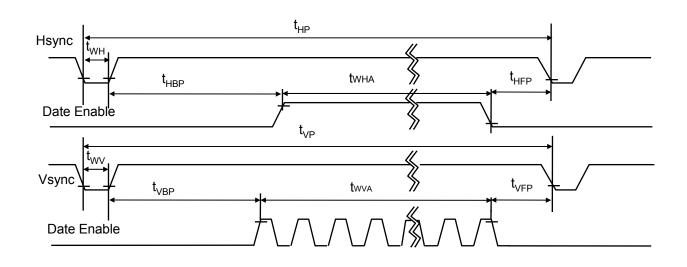
		Iable	5. IIMIN	GTABLE			
ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	62	65	68	MHz	15.4ns
Hsync	Period	tHP	1206	1344	1364	tour	
	Width	twн	8	136	-	tclk	
Vsync	Period	tVP	780	806	830	t up	
	Frequency	fv	60	60	60	tHP	
	Width	tw∨	1	6	24		
Data	Horizontal back porch	thbp	16	160	-	tour	
Enable	Horizontal front porch	thfp	16	24	-	tclk	
	Vertical back porch	tvвр	7	29	-	tup	
	Vertical front porch	tvfp	1	3	-	tHP	

Table 5. TIMING TABLE

3-4. Signal Timing Waveforms

Condition : VCC = 3.3V

Data Enable, Hsync, Vsync



High: 0.7VCC

Low: 0.3VCC

3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

									Inp	out Co	olor D	ata							
	Color			R	ED			GREEN							BL	UE			
	MSE						MSE					LSB						LSB	
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0		B 4	B 3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1		0	0	0	0	0	0		0	0	0	0	0
	Green	0	0	0	0	0	0		1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																			
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0		0	0	0	0	0		1	1	1	1	1

Table 6. COLOR DATA REFERENCE



3-6. Power Sequence

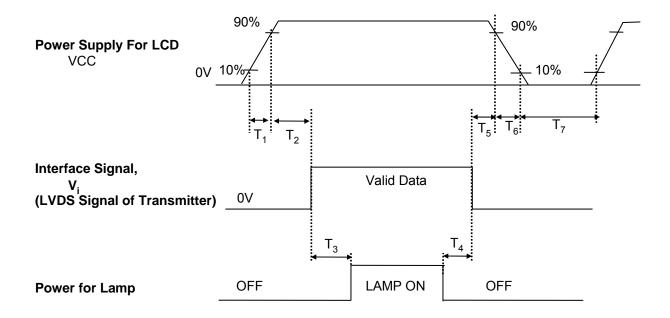


Table 7. POWER SEQUENCE TABLE

Parameter		Value		Unit
	Min.		Max.	
T ₁	-	-	10	ms
T ₂	0	-	50	ms
T ₃	200	-	-	ms
T ₄	200	-	-	ms
T ₅	0	-	50	ms
T ₆	0	_	10	ms
T ₇	400	_	_	ms

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

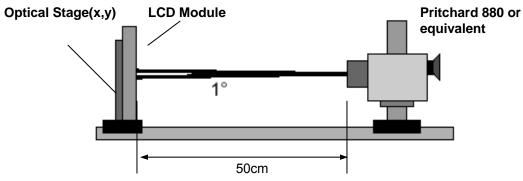


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz Dclk= 65MHz, IBL= 6.0mA

D	e ve ve et e v	Cumphiel		Values		Linita	Natas
P	arameter	Symbol	Min	Тур	Мах	Units	Notes
Contrast Ratio		CR	175	250	-		1
Surface Lumin	ance, white (5P)	L _{WH}	130	160		cd/m ²	2
Luminance Va	riation (13P)	δ_{WHITE}	-	-	1.65		3
Response Tim	e						4
	Rise Time	Tr _R	-	10	20	ms	
	Decay Time	Tr _D	-	20	30	ms	
Color Coordina	ates						PR650 or equivalent
	RED	RX	0.557	0.587	0.617		
		RY	0.313	0.343	0.373		
	GREEN	GX	0.300	0.330	0.360		
		GY	0.513	0.543	0.578		
	BLUE	BX	0.129	0.159	0.189		
		BY	0.118	0.148	0.178		
	WHITE	WX	0.283	0.313	0.343		
		WY	0.299	0.329	0.359		
Viewing Angle							5
>	α axis, right(Φ=0°)	Θr	40	45	-	degree	
>	α axis, left (Φ=180°)	ΘΙ	40	45	-	degree	
Ŋ	/ axis, up (Φ =90°)	Θu	10	15	-	degree	
y	/ axis, down (Φ =270°)	Θd	30	35	-	degree	
Gray Scale							6



Product Specification

Notes 1. Contrast Ratio(CR) is defined mathematically as :

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- Surface luminance is the average of 5 points across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1., When I_{BI} =6.0mA.
- 3. The variation in surface luminance, The Panel total variation (δ_{WHITE}) is determined by measuring L_{ON} at each test position 1 through 13, and then dividing the maximum L_{ON} of 13 points luminance by minimum L_{ON} of 13 points luminance. For more information see FIG 2.

 $\delta_{\text{WHITE}} = \text{Maximum}(L_1, L_2, \dots, L_{13}) / \text{Minimum}(L_1, L_2, \dots, L_{13})$

- Response time is the time required for the display to transition from white to black(RiseTime, Tr_{R}) and from black to white (Decay Time, Tr_{D}). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray s	cale specification	* f _v =60Hz
	Gray Level	Luminance [%] (Typ)
	LO	0.39
	L7	1.20
	L15	4.50
	L23	11.3
	L31	22.0
	L39	38.0
	L47	57.5
	L55	80.0
	L63	100



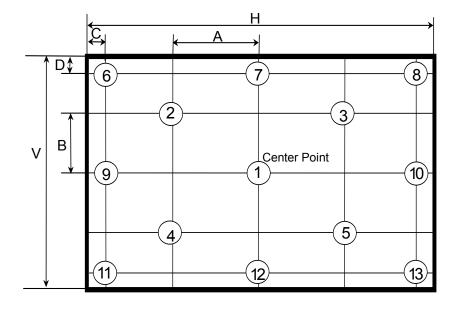
FIG. 2 Luminance

<measuring point for surface luminance>

POINTS: 5 POINT (1~5)

<measuring point for luminance variation>

POINTS: 13 POINTS (1~13)



A : H/4 mm B : V/4 mm C: 10 mm D:10 mm H: 304.128 mm V:228.096 mm @ H, V : Active Area

FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

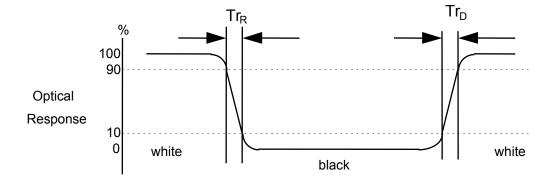
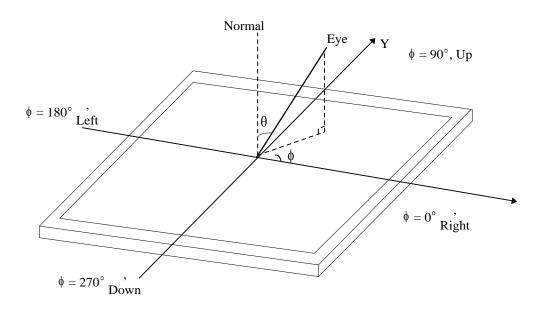




FIG. 4 Viewing angle

<Dimension of viewing angle range>





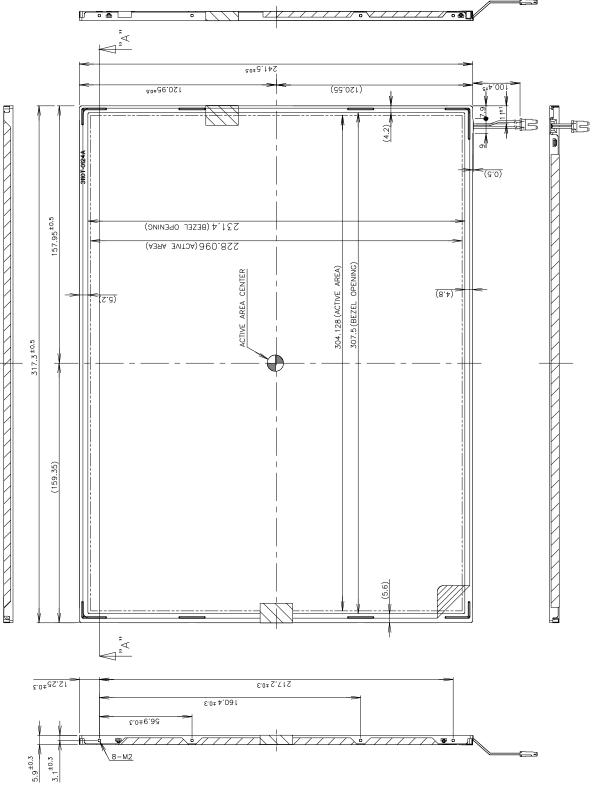
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP150X08. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	317.3 ± 0.5mm			
Outline Dimension	Vertical	241.5 ± 0.5mm			
	Depth	$5.9\pm0.3 mm$			
Bezel Area	Horizontal	307.5 ± 0.5 mm			
Dezel Alea	Vertical	231.4 ± 0.5mm			
Active Display Area	Horizontal	304.128 mm			
Active Display Area	Vertical	228.096 mm			
Weight	530g (Typ.) 545g (Max.)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

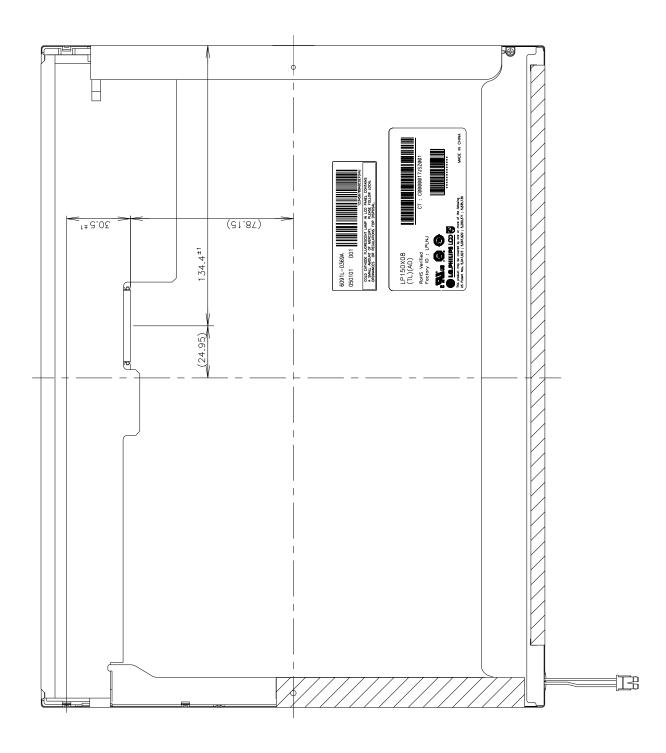


<FRONT VIEW>

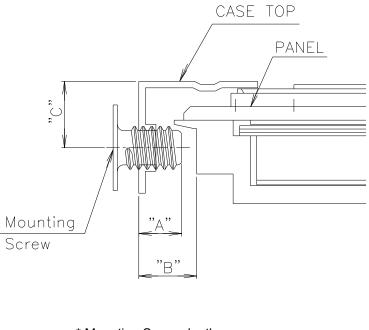




<REAR VIEW>



[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



* Mounting Screw depth depth Min.: "A" =2.0 depth Max: "B" =2.5
* Mounting hole location: "C" = 3.1(typ.)
*Torque: 2 kgf.cm(Max) (Measurement gauge: torque meter)

Notes : 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

6. Reliability

Environment test condition

No.	Test Item	Conditions						
1	High temperature storage test	Ta= 60°C, 240h						
2	Low temperature storage test	Ta= -20°C, 240h						
3	High temperature operation test	Ta= 50°C, 50%RH, 240h						
4	Low temperature operation test	Ta= 0°C, 240h						
5	Vibration test (non-operating)Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis							
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)						
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr						

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

c) EN 60950 : 2000, Third Edition

IEC 60950 : 1999, Third Edition

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992

b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.

c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH) E : MONTH D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 30 pcs
- b) Box Size : 482mm X 371mm X 325mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™)

D. += #	D		<u>v</u> -	lue	Value	
Byte#	Byte#	Field Name and Comments		EX)		
(decimal)	(HEX)		_	_	,	
0	00	Header	D	0 F	7777 7777 0000 0000	
1 2	01 02		F	F	ייייי ייייי	
3	03		F	F	TTT TTTT	HEADER
4	04		F	F	TTT TTT	
5	05		F	F	TIT III	
Ь	OL		F	F	TTT TTT	
7	07		0	٥	0000 0000	
8	08	EISA manufacturer code = LPL	ы	5	0011 0010	
9	09	Compressed ASCI	0	C	0000 JJ00	
10	DA	PanelSupplierReserved -Productcode	5	4	0010 0100	
լլ	OB	(Hex1LSB first)	0	l	0000 0001	
15	DC	ID (32-bit) serialnum ber= don′tcare	0	٥	0000 0000	VENDER
13	۵D		0	0	0000 0000	PRINCTID
14	OE		0	0	0000 0000	Trabberra
15	OF		0			
16	10	⊎eek ofmanufacture = don ′tcare	0	0	0000 0000	
17	11	Year of manufacture = 2007	1	l	0001 0001	
	15			ľ	0000 0007 0001 0001	
<u>18</u> 19	13	EDD Revision # = "2"		5	0000 0070	E DID VERSION REVISION
20	14	Video input definition = Digital I/p-non TM DS CRGB	8		7000 0000	NL VISION
21	15	Max H in age size () = 30.4128 (30)	l	E	0007 7770	DIS PIAY
22	16	Max V in age size ()= 22-8096 (22)	ľ	6	0007 0770	PARAMETER
23	17	D is play gamma = 2.2	7	8	0111 1000	
24	18	Feature support(DPMS) = Active off1RGB Cobr	0	A	0000 1010	
25	19	Red/Green bw Bits (RxRy/GxGy)	7	4	0111 0100	
26	ЪА	Blue/Whine Low Bins (BxBy/WxWy)	В	٥	1011 0000	
27	lΒ	Red X Rx = 0.587	9	6	1001 0110	
85	ЪС	Red Y Ry = 0-343	5	7	0707 0777	
29	lD	G reen X G x = 0.330	5	4	0707 0700	COLOR
30	ĴΕ	G reen Y G y = 0-543	8	В	7000 7077	CHARACIERSTIC
31	1F	Blue X Bx = 0.159	2	8	0010 1000	
32	20	Blue Y By = 0.148	2	5		
<u>33</u> 34	22 27	ຟ h bbe X ຟ x = 0-31.3 ຟ h bbe Y ຟ y = 0-329	5	0 4	0707 0700 0707 0000	
35	23		0	9	0000 0000	T C'TADIICI E D
36	24	Established Tinning I Established Tinning II	0	0		ESTABLISHED
			0			TIMINGS
37	25 Sr	Manufacturer's Timings		l		
38	26	Standard Timing D1 (Olh if notused)				
39	27	Standard Timing D1 (Olh if notused)		1	0000 0001	
40	28	Standard Timing 102 (01)h if notused)		1	0000 0001	
41.	29	Standard Timing 102 (01) hif notused)		1		
42	24	Standard Timing D3 (Ollh if not used)	0	l		
43	2B	Standard Timing D3 (Olh if notused)	٥	l	0000 0001	
44	5C	Standard Timing 104 (01.h if notused)	0	l	0000 0001	S'TANDARD
45	2D	Standard Timing 104 (01:h if notused)	٥	l	0000 0001	TIMING ID
46	35	Standard Timing 105 (01:h if notused)	0	l	0000 0001	
47	2F	Standard Timing 105 (Olh if notused)	0	l	0000 0001	
48	30	Standard Timing Db (Olh if notused)	0	l	0000 0001	
49	31	Standard Timing IDL(Olh if not used)	0	l	0000 0001	
50	32	Standard Timing 107(01:h if not used)	0	ľ	0000 0001	
51	33	Standard Timing 107 (01 hif notused)	0	l	0000 0001	
52	34	Standard Timing D& (Dlh if notused)	0	l	0000 0001	1
53	35	Standard Timing D& (Dlh if notused)	0	l	0000 0001	1
LL			5	تد ا		



Byt⊵#	Byte#		Va	lue	Value	
(decimal)	(HEX)	Field Name and Comments		EX)	(binary)	
54	36	Detailed Tining Descriptor#1.	6	4		
55		1024X768 @ 60 Hz mode :pixe;clock = 65-00 M Hz	1	9	0007 7007 0070 0770	
56		Horizontal A ctive = 1024 pixels		0		
57		HorizontalBlanking = 320 pixels	4	0	0000 0000	
58	AE	HorizontalActive : HorizontalBlanking	4	l	100 0007 2000 0007	
59	 3B	Vertical Avoire = 768 lines		0		
60	3C	VerticalBlanking = 38 lines	2	6	0010 0110	DETAILED
61	ЭD	VerticalActive : VerticalBlanking	3	0	0011 0000	TIMING
62	Œ	HorizontalSync.0ffset=24 pixels	l	8	0001 1000	DESCRIPTION
63	ЗF	Horizontal Sync Pulse Width = 136 pixels	8	8	1000 1000	#1
64	40	Vertical Sync 0 ffset = 3 lines : Sync U idth = 6 lines	З	6	0011 0110	
65	4ጌ	HorizontalVertical Sync 0ffset/Width upper2bits	0	0	0000 0000	
66	42	Horizontal In age Size = 304.128 (304)	З	0	0011 0000	
67	43	Vertical In age Size = 228-096 (228)	Ε	4	1110 0100	
68	44	Horizontal& Vertical In age Size	l	۵	0001 0000	
69	45	Horizonta1Border = 0	٥	٥	0000 0000	
70	46	VerticalBorder=0	٥	٥	0000 0000	
71	47	Non-interlaced n Nomn ald isplay no stereonDigital separate synch IV polnegatives	l	8	0007 7000	
72	48	Detailed Timing Descriptor #2 was not used	0	٥	0000 0000	
73	49		٥	٥	0000 0000	
74	4A		٥	٥	0000 0000	
75	4B		٥	٥	0000 0000	
76	4C		0	0	0000 0000	
77	4⊅		0	0	0000 0000	
78	4E		0	0	0000 0000	DETAILED
79	4F		0	0	0000 0000	TIMING
80	50		0	0	0000 0000	DESCRIPTION
81	51			0		#2
58	52			0		
83	53					
84	55					
85	55					
86 87	<u>56</u> 57			0	0000 0000	
<u>88</u>	<u> </u>					
89	 59			0		
90	5A	Detailed Tining Descriptor #3		0		
 91	 58			0	0000 0000	
 92	57			0		
93	5D	ASCID) ata String Tag (Supplier Name)	F	E	7777 7770 2000 2000	
<u>, 1</u> 94	 5E			0		
95	 5F	L	4	C	0700 7700	
<u>,,</u> 96	 60	G	4	7	1110 0111 1110 0010	DETAILED
<u>.</u> 97	61 61	P	5	Ō	0101 0000	TIMING
98	62	h	6	8	0110 1000	DESCRIPTION
99	63	i	6	9	0770 7007	#3
100	64	1	6	С	0110 1100	
101	65	i	6	9	0110 1001	
105	66	p	7	0	0777 0000	
103	67	S	7	З	0111 0011	
104	68	L	4	С	0700 7700	
105	មា	C	4	З	0700 0077	
106	ЬA	D	4	4	0700 0700	
107	ЬB	LF	۵	Α	0000 1010	



Byte#	Byt⊵#	Field Name and Comments	٧a	lue	Value	
(decimal)	(HEX)			EX)	(binary)	
108	ЬC	Detailed Timing Descriptor #4	0	0	0000 0000	
109	ЬD		0	0	0000 0000	
110	ЬE	ASCILData Storing Tag (Supplier P/N)	0	0	0000 0000	
lll	ЬF		F	Ε	7777 777O	
115	70		0	0	0000 0000	
113	7L	L	4	С	0100 1100	
114	72	Р	5	0	0707 0000	
115	73	l	Ы	l	0011 0001	DETAILED
116	74	5	Ы	5	0011 0101	TIMING
117	75	0	Ы	0	0011 0000	DESCRIPTION
118	7ь	X	5	8	0707 7000	#4
119	77	0	Ы	0	0011 0000	
150	78	B	Ы	8	0011 1000	
151	79	-	2	D	0010 1101	
155	7A	Т	5	4	0707 0700	
153	7B	L	4	C	0100 1100	
124	7C	А	4	l	0700 0007	
125	7D	D	4	4	0700 0700	
126	7E	Extension flag = 00	0	0	0000 0000	EXTENSION FIAG
127	7F	Checksum	7	9	0777 7007	CHECKSUM