SmarterGlass

state-of-the-art display solutions

www.smarterglass.com 978 997 4104 sales@smarterglass.com SPEC

Ī	Spec No.	TQ3C 8EAF0 E1DDP46 00
I	Date	April 10, 2008

TYPE : TCG057VGLBB-G20

< 5.7 inch VGA transmissive color TFT with LED backlight, Constant current circuit for LED backlight and touch panel >

CONTENTS

1. Application

- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Backlight characteristics
- 10. Design guidance for analog touch panel (T/P)
- 11. Lot number identification
- 12. Warranty
- 13. Precautions for use
- 14. Reliability test data
- 15. Outline drawing



KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: I	Engineering de	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 10, 2008	D. Ajisaka	70. Johnson	G. Matsumoto	.J. Sakaguchi	Zo , Staf

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP46-00	TCG057VGLBB-G20	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



$\begin{array}{ c c c c c c } \hline \begin{tabular}{ c c c c c c } \hline \hline TQG05TVCLBB-G20 & $$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$	Page
Date Designed by : Engineering dept. Confirmed by : QA dept Prepared Checked Approved Checked Approved	-
Date Prepared Checked Approved Checked Approved	
Prepared Checked Approved Checked Approved	
Rev.No. Date Page Descriptions Image: I	d
Rev.No. Date Page Descriptions	



1. Application

This document defines the specification of TCG057VGLBB-G20. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input)
Touch panel	(with Constant current circuit for LED Backlight) : Analog type, Non-Glare treatment

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions	$127.2 \text{ (W)} \times 100.4 \text{ (H)} \times 6.75 \text{ (D)}$	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	640×(B,G,R) (W) × 480 (H)	dot
Dot pitch	$0.06 \text{ (W)} \times 0.18 \text{ (H)}$	mm
Base color *1	Normally White	-
Mass	135	g

3-1. Mechanical specifications of LCD

*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Mechanical specifications of touch panel

Item	Item Specification	
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.05 ~ 0.8	N
Transmittance	Тур. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



4. Absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V_{DD}	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{\rm IN}B$	V
Brightness adjust voltage	VBRT	0	$V_{\rm IN}B$	V
Supply voltage for touch panel	V_{TP}	0	6.0	V
Input current of touch panel	I_{TP}	0	0.5	mA

4-1. Electrical absolute maximum ratings

*1 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, HSYNC, VSYNC, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	°C
Storage temperature	*2	Тѕто	-30	80	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Нѕто	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -30°C < 48h , Temp. = 80°C < 168h

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

*3 Non-condensing

*4 Temp. 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	$10 \sim 55 \; \mathrm{Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

```
*6 Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms
```

```
3 times in each direction: \pm X, \pm Y, \pm Z
EIAJ ED-2531
```

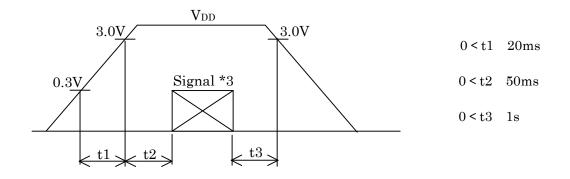
KYOCERa

5. Electrical characteristics

5-	1.	LCD

					Temp. = -2	0~70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	V_{DD}	-	3.0	3.3	3.6	V
Current consumption for logic	I_{DD}	*2	-	170	220	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
I	VIL	"Low" level	0	-	$0.3 V_{DD}$	V
Input signal voltage *3	VIH	"High" level	$0.7 V_{DD}$	-	V _{DD}	V

*1 $V_{\text{DD}}\text{-turn-on conditions}$



*2 Display pattern:

*3 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

5-2. Touch panel

Item	Specification
Supply voltage for touch panel	5.0V
	$xL \sim xR : 200\Omega \sim 1,000\Omega$
Terminal resistance	$yU \sim yL : 200\Omega \sim 800\Omega$
Linearity	less than ±2.0%
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$



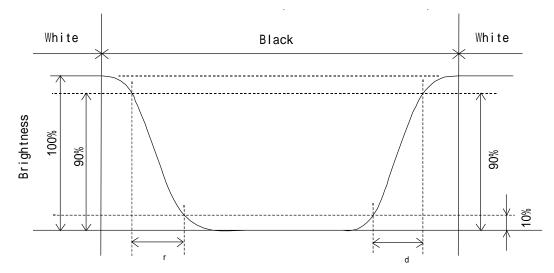
Spec No.Part No.PageTQ3C-8EAF0-E1DDP46-00TCG057VGLBB-G204

Measuring spot = 6.0mm, Temp. = 25°C Item Symbol Condition Min. Typ. Max. Unit = =0° --Rise 10 \mathbf{ms} $\tau_{\,r}$ Response time = =0° --Down 25 τ d \mathbf{ms} -80 -UPPER Viewing angle range deg. -80 -LOWER View deriction CR 5 : 6 o'clock LEFT -80 -(Gray inversion) deg. ϕ right -80 -= =0° Contrast ratio \mathbf{CR} 300 500--Brightness \mathbf{L} IF=15mA/Line 140200 cd/m^2 0.560.610.66х = =0° Red 0.320.370.42у 0.290.340.39х =0° Green = 0.520.570.62у Chromaticity coordinates 0.09 0.140.19 х Blue =0° = 0.060.110.16у 0.280.330.38х White = =0° 0.30 0.350.40 у

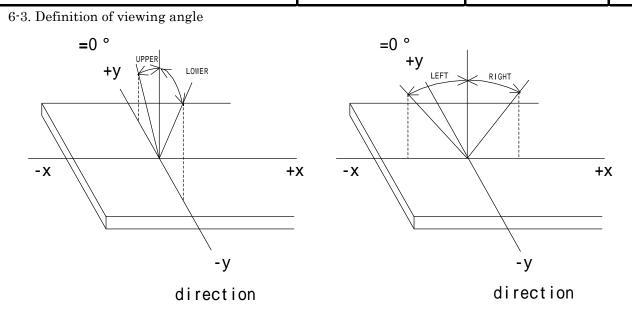
6. Optical characteristics

6-1. Definition of contrast ratio

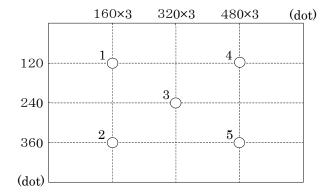
6-2. Definition of response time







6-4. Brightness measuring points



1) Rating is defined on the average in the viewing area.

2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25° C)



7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	Ι	
3	H _{SYNC}	Horizontal synchronous signal (negative)	Ι	
4	VSYNC	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	V _{DD}	3.3V power supply	-	
29	V _{DD}	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	Ι	
32	NC	No connect	-	
33	VINB	Power supply for LED backlight	-	
34	VINB	Power supply for LED backlight	-	
35	VINB	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H : ON , L : OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight	-	
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	

7-1. Pin assignment of LCD panel and LED

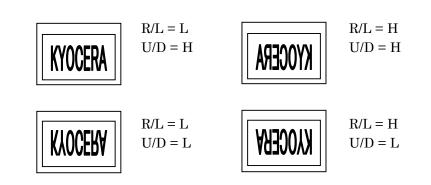
LCD connector Recommended matching FFC or FPC : IMSA-9681S-40A-GF (IRISO)

: 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP46-00	TCG057VGLBB-G20	7

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.



7-2. Pin assignment of touch panel

*2

No.	Symbol	Description
1	уL	y-Lower terminal
2	xL	x-Left terminal
3	уU	y-Upper terminal
4	xR	x-Right terminal

Touch panel side connector	:	1.25mm pitch	
Recommended matching connector	:	Series FE, FFS	(JST)
	:	KCA-K4R	(DMC)



8. Input timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Clock	Frequency	1/Tc	-	25.18	28.88	MHz	
CIOCK	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	10	-	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
	Cycle	TH	30.0	31.8	-	μs	
Horizontal sync. signal			770	800	900	clock	
	Pulse width	THp	5	30	-	clock	
	Set up time	THs	10	-	-	ns	
	Hold time	THh	10	-	-	ns	
	Cycle	TV	515	525	560	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	10	-	-	ns	
	Hold time	TVh	10	-	-	ns	
	Pulse width	TEp	640			clock	
Enable signal (ENAB)	Set up time	TEs	10	-	-	ns	
()	Hold time	TEh	10	-	-	ns	
H _{SYNC} - Enable s	ignal phase difference	THE	112	144	175	clock	
H _{SYNC} - V _{SYNC} sig	nal phase difference	THV	10	-	-	ns	
Vertical sync. sig	nal start position	TVE	2	35	76	line	
Horizontal displa	ay period	THd	640			clock	
Vertical display	period	TVd		480		line	

8-1. Timing characteristics

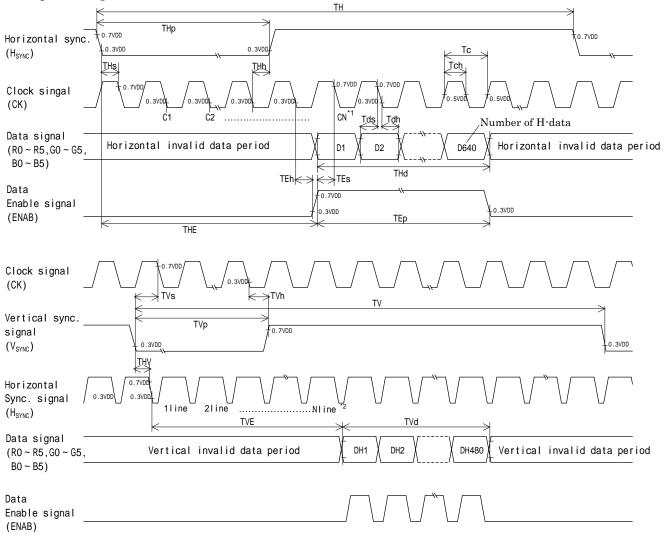
*When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock) as shown in 8-3.

*When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line) as shown in 8-3. *In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Input Data Signals and Display position on the screen

B G	R			
	B G	B G R	B G R	B G R





8-3. Input timing characteristics

 $\ast 1$ When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock).

 $\ast 2$ When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line).



Spec No.Part No.PageTQ3C-8EAF0-E1DDP46-00TCG057VGLBB-G2010

Temp.=25

9. Backlight characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN}B$	(3.0)	-	(5.5)	V	Ta=-20 ~ 70°C
ON-OFF (H)	DIEN	(0.8 V _{IN} B)	-	VINB	V	-
ON-OFF (L)	BLEN	0	-	$(0.2V_{IN}B)$	V	-
LED forward current *1, *2	IF	(14)	(15)	(16)		VBRT=0~(1.4)V
LED forward current 1, 2	11	(2.8)	(3.0)	(3.2)	mA	VBRT=(2.8)V
Sumply any state	ID	-	(440)	(570)	mA	V _{IN} B=3.3V, IF=15mA
Supply current	IINB	-	(280)	(360)	mA	V _{IN} B=5.0V, IF=15mA
Operating life time *3, *4	Т	-	(40,000)	-	h	IF=15mA, Ta=25

*1 For each "LED"

*2 An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

*3 When brightness decrease 50% of initial brightness.

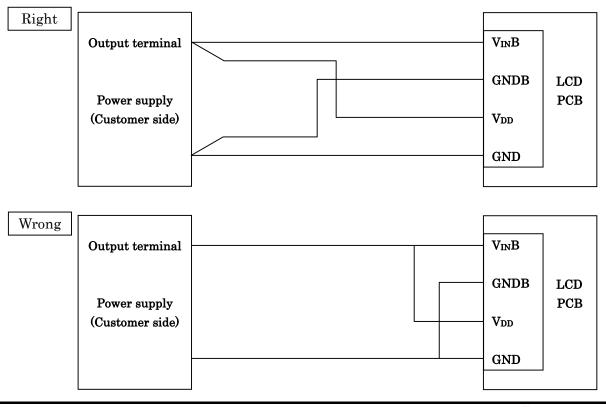
*4 Life time is estimated data. (Condition : IF=15mA, Ta=25 in chamber)

* When you start-up, please charge in sequence of $V_{\rm IN}B\mbox{->}BLEN,$ or VBRT.

When you shut-down, please stop in sequence of BLEN and/or VBRT->VINB.

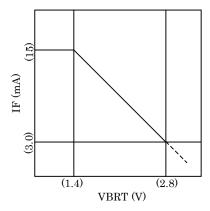
* Please do not connect the other than our backlight to this output connector on the PCB.

* In case V_{DD} and V_{IN}B are supplied by a single power source, V_{DD} & V_{IN}B, and GND are connected directly and separately from the output on the power source. If the common wire are used for V_{DD} & V_{IN}B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





* VBRT-IF characteristics



10. Design guidance for analog touch panel (T/P)

10-1 Electrical (In customer's design, please remember the following considerations.)

- 1 Do not use the current regulated circuit.
- 2 Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maxim um ratings" for details.)
- 3 Analog T/P can not sense two points touching separately.
- 4 A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the T/P position data.
- 5 Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2 Software

- 1 Do the "User Calibration".
- 2 "User Caribration" may be needed with long term using. Include "User Caribration" menu in your software.
- 3 When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3 Mounting on display and housing bezel

- 1 Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2 Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- 3 If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

- No1. No5. above indicate
 - 1. Year code
 - 2. Month code
 - 3. Date
 - 4. Version Number
 - 5. Country of origin (Japan or China)

 $\downarrow 5$

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



13. Precautions for use

- 13-1. Installation of the LCD
 - 1) The LCD shall be installed so that there is no pressure on the LSI chips.
 - 2) The LCD shall be installed flat, without twisting or bending.
 - 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.
- 13-3. LCD operation
 - 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
- Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



14. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Polyacetal stylus (R0.8)oneHitting force 3N Hitting speed 2 time/smillion		Terminal resistance Insulation resistance Linearity Actuation Force	 No defect No defect No defect No defect

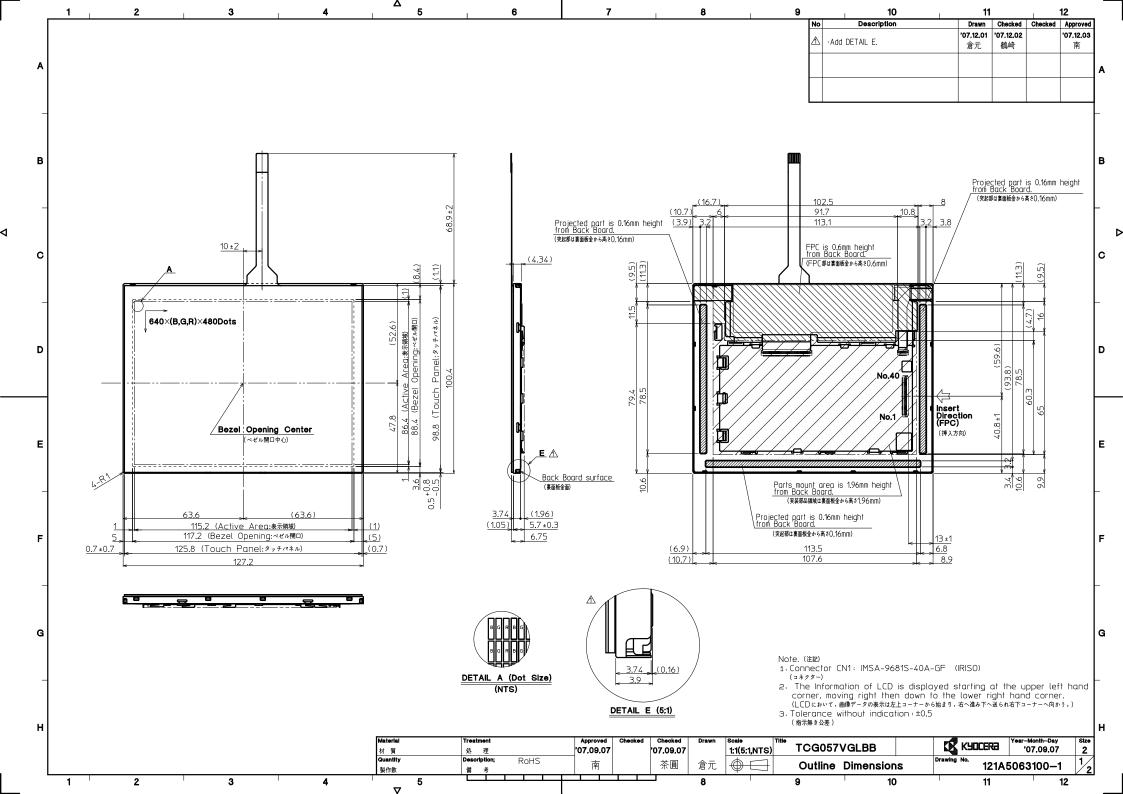
* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

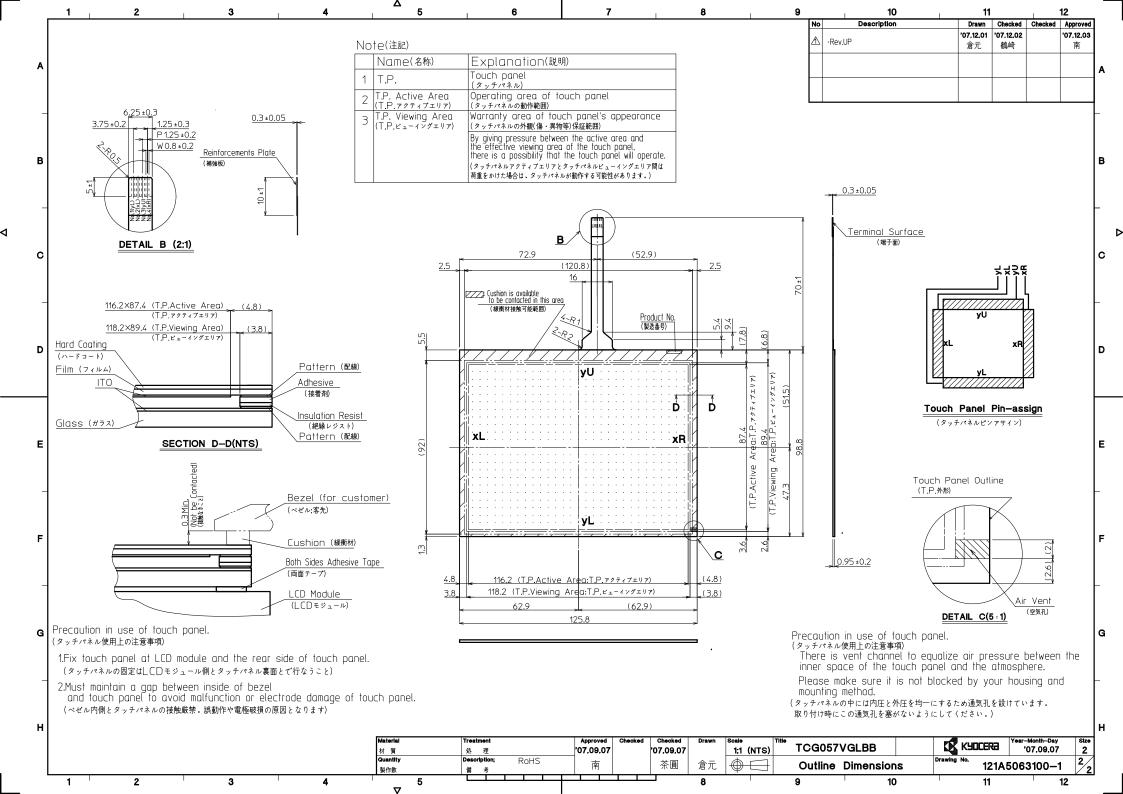
* The LCD is tested in circumstances in which there is no condensation.

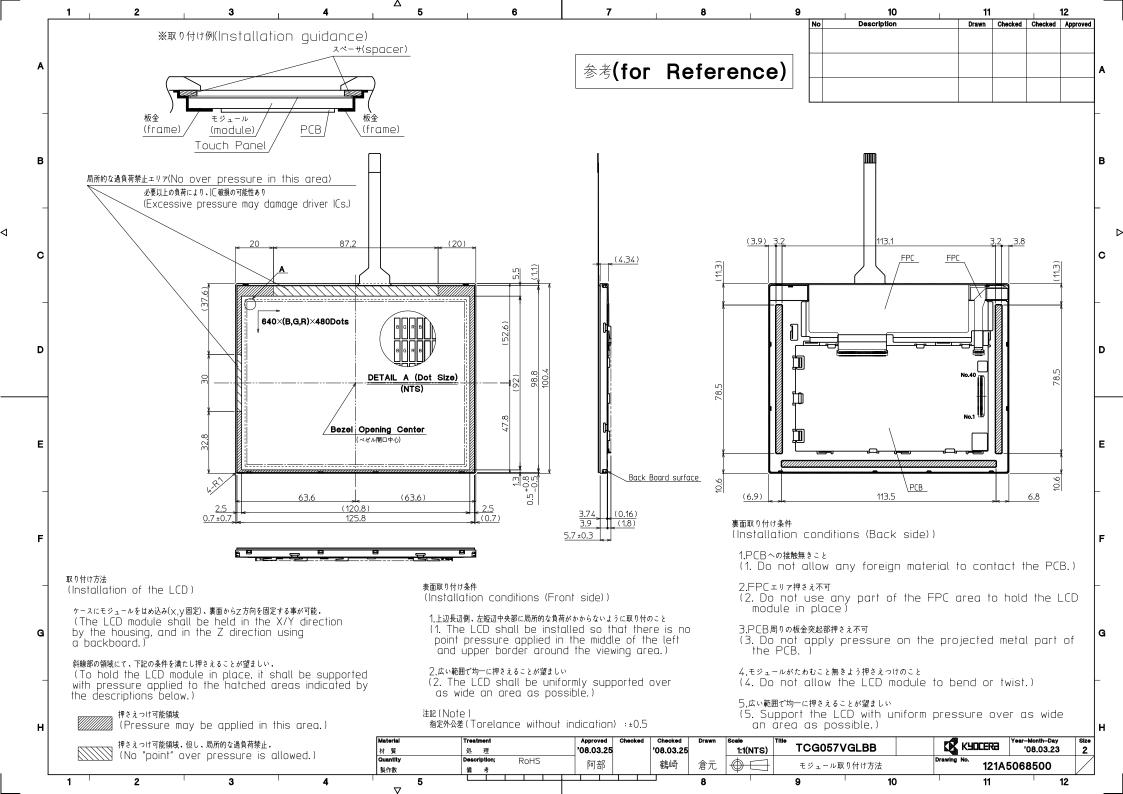
* The reliability test is not an out-going inspection.

* The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.









Spec No.	TQ3C 8EAF0 E2DDP46 00
Date	April 10, 2008

KYOCERA INSPECTION STANDARD

TYPE : TCG057VGLBB-G20

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 10, 2008	D.Ajisaka	70 Johnword	4 Matricmoto	.J. Sakaguchi	76 , Jul



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP46-00	TCG057VGLBB-G20	-

	Revision record								
	Dete			Engineering d		Confirmed by	: QA dept.		
	Date	Prepared		Checked	Approved	Checked	Approved		
Rev.No.	Date	Page			Descripti	ions			
					*				



	Note							
General	 Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 							
	3. Inspection conditions							
	Lumina		: 500 Lux min.					
	Inspect	ion distance	: 300 mm.					
	Tempe	rature	:25 ± 5					
	Directi	on	: Directly above					
Definition of inspection item	Dot defect	Bright dot defect Black dot defect Adjacent dot	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter Count dot: If the dot is visible through the filter Don't count dot: If the dot is not visible through the filter R G B R G B R G B R G B R G B R G B dot defect The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen. Adjacent dot defect is defined as two or more bright dot defects. R G B R G B R G B R G B R G B R G B dot defect					
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	RGBRGBRGB White") and not operating.					
		Appearance	Does not satisfy the value at the spec.					
	Others	inspection LED wire	Damaged to the LED wire, connector, pin, functional					
	Others		failure or appearance failure.					
	Definition of size	finition Definition of circle size Definition of linear size						
		d = (a + b)/2						

Visuals specification



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP46-00	TCG057VGLBB-G20	2

2) Standard

2) Standard Classification Inspection item		Judgement standard						
Defect	*							
(in LCD	defect	Digit dot delect				-		
(in LCD glass)	derect	Black dot defect		Bright dot spacing Acceptable number		5 mm or more		
giass)		Black dot defect		-		:5 · =		
		2 dot join Bright dot defect Black dot defect		Bright dot spacing : 5 mm o			l or more	
				Acceptable number : 2				
				Acceptable number		: 3		
		3 or more of	dots join	Acceptable number		: 0		
		Total dot d	-	Acceptable number		: 5 Ma	X	
	Others	White dot,		*				
		(Circle)		Size (mn	n)	Ac	ceptable number	
		(,		d	0.2	110	(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
D . 1		D1 · (a . 1)					
	External inspection Polarizer		Scratch)			``		
(Defect on				Width (mm)	Length (mm)		Acceptable number	
Polarizer				W 0.1	- L	5.0	(Neglected)	
between F				0.1 < W = 0.3	5.0 < L	5.0	(Neglected) 0	
and LCD	glass)			0.3 < W	J.0 V L		0	
							•	
		Polarizer (Bubble)	~ (``			
				Size (mn		Acceptable number		
				d 0.2		(Neglected)		
				$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		5 3		
				0.5 < d	0.0		0	
							0	
		Foreign pa						
		(Circle sh	ape)	Size (mm)		Acceptable number		
				$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		(Neglected)		
				$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			<u>5</u> 3	
			0.4 < d = 0.5 0.5 < d			0		
		u			~			
Foreign particle								
(Linear shape)				ength (mm) Acceptable num				
		Scratch		W 0.03			(Neglected)	
			0.02 < W = 0.1	L 2.0		(Neglected)		
			0.03 < W = 0.1	2.0 < L 4.0		3		
				0.1 < W	4.0 < L		0 (Assembling to	
				0.1 × W	-		(According to	
							circular shape)	



Spec No.Part No.PageTQ3C-8EAF0-E2DDP46-00TCG057VGLBB-G203

Inspection item		Jı	ıdgemei	nt stand	lard				
Scratch,	(W = Width, L = Length, D = Diameter = (major axis + minor axis)/ 2)								
Foreign particle	Item Width(mm) Length(mm) Acce							ceptable number	
(Touch screen		0.08 < d	L	4	1pce	1pces within 30mm			
portion)	Scratch	0.05 < d	0.08	L	6	2pce	${ m s}$ within	20mm	
	Scratch	0.03 < d	0.05	L	10	2pce	${ m s}$ within	20mm	
		d	0.03	L	20		Neglecte	ed	
	Foreign	0.05 < W	0.1	L	5	2pces	s within	30mm	
	(line like)	W	0.05	Negl	ected		Neglected		
	Foreign	0.2	< D	0.3		2pce	s within	30mm	
	(circle like)		D	0.2			Neglecte	ed	
		lied to the visib					_		
		are foreign pa			0			·	
	electrical perf	ormance out of t	the activ	ve area,	we app	prove o	of this pro	duct.	
Glass crack	Item		Size (n	nm)			Accep	otable	
(Touch screen	Item				- r - r		number		
portion)				l I	Х	3			
	Conner	××××××××××××××××××××××××××××××××××××××					2 r	ocs	
	crack				Y	3 /par			
				Z		I			
				2	< t				
	Crack in				Х	5			
	other area					2 g	ocs		
	than in				Y	1.5	/side		
	corner	2		Z	< t				
		- 1							
					1				
					/ /				
	Progressive		0 p	DCS					
	crack		\sim	\sim	\sim			en 1pcs)	
			\rightarrow						
	Above are app	lied to the visib	le area.						
		are foreign p		and da	mage	affecte	d serious	ly to the	
		rmance out of t							
							-		
Newton's ring	Neglected.								
	_								
	[\bigcap						
)) +	N	lewton'	s ring		

